

Practical Datesheet (April 2020 Exam Session)

Practical Date		05-Aug-20		Group-A	Branch	IT Stream Only					
Practical Date	ED_RollNo	Adm_Type	studentname	BR_TITLE	SUB_TITLE	SUB_CODE	Sem	SUB_TP	ed_rtype	InternalExamFormStatus	ExternalExamFormStatus
05-Aug-20	1520512362	Reappear	Dhooj bahadur	Bachelor of Computer Applications	Software Lab-IV (Data Structures)	BSBC-305	3	P	RP	TRUE	TRUE
05-Aug-20	1520512362	Reappear	Dhooj bahadur	Bachelor of Computer Applications	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	RP	TRUE	TRUE
05-Aug-20	2011212345	New	LOVELY KUMARI	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212345	New	LOVELY KUMARI	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE
05-Aug-20	2011212346	New	Komalpreet Kaur	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212346	New	Komalpreet Kaur	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE
05-Aug-20	2011212347	New	Lovedeep Kaur	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212347	New	Lovedeep Kaur	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE
05-Aug-20	2011212348	New	HARPAL SINGH	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212348	New	HARPAL SINGH	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE
05-Aug-20	2011212349	New	Satwinder Singh	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212349	New	Satwinder Singh	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE
05-Aug-20	2011212350	New	RUPINDER SINGH	Bachelor of Computer Applications - Lateral Entry	Software Lab-IV (Data Structures)	BSBC-305	3	P	R	TRUE	TRUE
05-Aug-20	2011212350	New	RUPINDER SINGH	Bachelor of Computer Applications - Lateral Entry	Hardware Lab-I (Digital Circuits & Logic Design)	BSBC-306	3	P	R	TRUE	TRUE

Instructions to appear for practical viva on ZOOM Application:

1. Link will be shared with students one day before the viva.
2. Students will join the ZOOM APP at 09:30 AM sharp on day of viva (check datesheet) and viva will start immediately.
3. A waiting room will be created automatically in ZOOM APP. Students will be picked one by one from the waiting room and VIVA will be taken.
4. Timings are from 09:30 to 12:30 noon, so all students should be available from 09:30 to 12:30 noon.
5. If any student miss the viva on Zoom App then he/she will be marked absent.
6. To upload your all practical files (Regular/Reappear) click here <https://forms.gle/Rn9C6tN6YF6Nn4LJA>